Pin #	BANK	Name	Connect 1	Connect 2	Connect 3	Connect 4
A1	POWER	GND	GND			
A2	34	RP_GPIO4	J6			
A3	34	RP_GPIO17	J6			
A4	34	RP_GPIO27	J6			
A5	15	RP_GPIO22	J6			
A6	15	RP_GPIO10	J6	JP9(CUT)		
A7	15	RP_GPIO9	J6			
A8	15	RP_GPIO11	J6	JP11(CUT)		
A9	15	RP_GPIO5	J6			
A10	15	RP_GPIO6	J6	D3(BLUE)		
A11	15	AIN33_P				
A12	15	AIN33_N				
A13	15	AIN32_P				
A14	15	AIN32_N				
A15	POWER	GND	GND			
B1	34	RP_GPIO2	J6	U11		
B2	34	RP_GPIO3	J6	U11		
B3	34	Not connected				
B4	34	Not connected				
B5	34	HDMI_RX_HPD	J3			
B6	15	RP_GPIO14	J6			
B7	15	BANK15_VCCO	BANK15_VCCO			
B8	POWER	GND	GND			
B9	15	Not connected				
B10	15	RP_GPIO13	J6	D9(GREEN)		
B11	15	RP_GPIO19	J6	D8(RED)		
B12	15	RP_GPIO26	J6			
B13	15	CLK20M	X1			
B14	15	Not connected				
B15	15	RP_GPIO21	J6	SW4(STOP)		
C1	34	HDMI_TX_EN	J3			
C2	POWER	GND	GND			
C3	34	BANK34_VCCO	BANK34_VCCO			
C4	34	Not connected				
C5	34	HDMI2_RX_HPD	J3			
C6	15	CTRL_EN	P1			

Pin #	BANK	Name	Connect 1	Connect 2	Connect 3	Connect 4
C7	15	RP_GPIO15	J6			
C8	15	RP_GPIO23	J6			
C9	15	RP_GPIO25	J6			
C10	15	RP_GPIO7	J6			
C11	15	BANK15_VCCO	BANK15_VCCO			
C12	POWER	GND	GND			
C13	15	RP_GPIO16	J6	SW6(RUN)		
C14	15	RP_GPIO20	J6	SW5(START)		
C15	15	Not connected				
D1	34	HDMI2_TX_EN	J3			
D2	34	Not connected				
D3	34	Not connected				
D4	34	Not connected				
D5	0	N90495	VCC1V8			
D6	POWER	GND	GND			
D7	15	RP_GPIO18	J6	J15	J11	LED2(WS2812B)
D8	15	RP_GPIO24	J6			
D9	15	RP_GPIO8	J6			
D10	15	RP_GPIO12	J6			
D11	15	Not connected				
D12	15	PMOD2_4	J2			
D13	15	PMOD2_8	J2			
D14	POWER	GND	GND			
D15	15	PMOD1_1	J1			
E1	34	HDMI2_TX_HPD	J3			
E2	34	HDMI_TX_HPD	J3			
E3	34	Not connected				
E4	POWER	GND	GND			
E5	0	TCK_FPGA	U8			
E6	0	VCC3V3	VCC3V3			
E7	POWER	GND	GND			
E8	POWER	VCC1V0	VCC1V0			
E9	POWER	GND	GND			
E10	POWER	VCC1V0	VCC1V0			
E11	15	PMOD2_3	J2			
E12	15	PMOD2_7	J2			
E13	15	PMOD1_5	J1			

Pin #	BANK	Name	Connect 1	Connect 2	Connect 3	Connect 4
E14	15	PMOD1_6	J1			
E15	15	PMOD1_2	J1			
F1	34	HDMI2_TX_CEC	J3			
F2	34	HDMI_TX_CEC	J3			
F3	34	Not connected				
F4	34	Not connected				
F5	0	QSPI_SCK	JP11(CUT)	JP12(CUT)	U1	
F6	POWER	GND	GND			
F7	POWER	VCC1V0	VCC1V0			
F8	0	XADCGND	XADCGND			
F9	0	N52879	L7			
F10	POWER	GND	GND			
F11	15	PMOD2_6	J2			
F12	15	BANK15_VCCO	BANK15_VCCO			
F13	POWER	GND	GND			
F14	15	PMOD1_7	J1			
F15	15	PMOD1_3	J1			
G1	34	Not connected				
G2	34	BANK34_VCCO	BANK34_VCCO			
G3	POWER	GND	GND			
G4	34	Not connected				
G5	0	DONE	VCC3V3	D1		
G6	POWER	VCC1V0	VCC1V0			
G7	POWER	GND	GND			
G8	0	XADCGND	XADCGND			
G9	0	XADCGND	XADCGND			
G10	POWER	VCC1V0	VCC1V0			
G11	15	PMOD2_2	J2			
G12	15	PMOD2_5	J2			
G13	15	PMOD2_1	J2			
G14	15	PMOD1_8	J1			
G15	15	PMOD1_4	J1			
H1	34	EXT_CLK	J16			
H2	34	Not connected				
H3	34	FPGA_SDA	U11			
H4	34	FPGA_SCL	U11			
H5	0	CFGBVS	VCC3V3			

Pin #	BANK	Name	Connect 1	Connect 2	Connect 3	Connect 4
H6	POWER	GND	GND			
H7	POWER	VCC1V0	VCC1V0			
H8	0	XADCGND	XADCGND			
H9	0	XADCGND	XADCGND			
H10	POWER	GND	GND			
H11	15	Not connected				
H12	15	CTRL_R2	P1			
H13	15	CTRL_R1	P1			
H14	14	QSPI_DQ0	IC2			
H15	14	QSPI_DQ1	JP9(CUT)	JP10(CUT)		
J1	34	Not connected				
J2	34	Not connected				
J3	34	Not connected				
J4	34	Not connected				
J5	0	PROG	SW2			
J6	POWER	VCC1V0	VCC1V0			
J7	POWER	GND	GND			
J8	POWER	GND	GND			
J9	POWER	GND	GND			
J10	POWER	VCC1V0	VCC1V0			
J11	14	Not connected				
J12	14	QSPI_DQ2	IC2			
J13	POWER	GND	GND			
J14	14	BANK14_VCCO	BANK14_VCCO			
J15	14	HDMI2_TX_CEC_LS	J4			
K1	34	Not connected				
K2	34	Not connected				
K3	34	Not connected				
K4	0	TDO_FPGA	U8			
K5	0	TMS_FPGA	U7			
K6	POWER	GND	GND			
K7	POWER	VCC1V0	VCC1V0			
K8	POWER	GND	GND			
K9	POWER	VCC1V0	VCC1V0			
K10	POWER	GND	GND			
K11	14	PUDC_B	BANK14_VCCO			
K12	14	N64980	U1	JP11(CUT)	JP12(CUT)	

Pin #	BANK	Name	Connect 1	Connect 2	Connect 3	Connect 4
K13	14	QSPI_DQ3	IC2			
K14	14	HDMI2_TX_HPD_LS	J4			
K15	14	HDMI_TX_CEC_LS	J4			
L1	34	Not connected				
L2	POWER	GND	GND			
L3	34	BANK34_VCCO	BANK34_VCCO			
L4	0	TDI_FPGA	U7			
L5	0	INIT	VCC3V3			
L6	POWER	VCC1V8	VCC1V8			
L7	POWER	GND	GND			
L8	POWER	VCC1V8	VCC1V8			
L9	POWER	GND	GND			
L10	14	Not connected				
L11	14	QSPI_CS	IC2			
L12	14	Not connected				
L13	14	HDMI_TX_EN_LS	J4			
L14	14	HDMI2_TX_EN_LS	J4			
L15	14	HDMI_TX_HPD_LS	J4			
M1	34	Not connected				
M2	34	Not connected				
M3	34	Not connected				
M4	34	GPIO_DIPSW	SW3(DIP)			
M5	0	MODE0	SW7(DIP)			
M6	0	MODE2	SW7(DIP)			
M7	14	Not connected				
M8	14	Not connected				
M9	14	Not connected				
M10	14	Not connected				
M11	POWER	GND	GND			
M12	14	BANK14_VCCO	BANK14_VCCO			
M13	14	HDMI2_RX_HPD_LS	J4			
M14	14	Not connected				
M15	14	Not connected				
N1	34	S7_LED0	LED1(WS2812B)	J7		
N2	34	Not connected				
N3	34	Not connected				
N4	34	Not connected				

Pin #	BANK	Name	Connect 1	Connect 2	Connect 3	Connect 4
N5	0	MODE1	SW7(DIP)			
N6	14	Not connected				
N7	14	Not connected				
N8	14	Not connected				
N9	14	Not connected				
N10	14	Not connected				
N11	14	Not connected				
N12	14	Not connected				
N13	14	HDMI_RX_HPD_LS	J4			
N14	14	Not connected				
N15	14	Not connected				
P1	34	Not connected				
P2	34	Not connected				
P3	34	Not connected				
P4	POWER	GND	GND			
P5	0	VCC3V3	VCC3V3			
P6	14	Not connected				
P7	14	Not connected				
P8	POWER	GND	GND			
P9	14	BANK14_VCCO	BANK14_VCCO			
P10	14	Not connected				
P11	14	Not connected				
P12	14	Not connected				
P13	POWER	GND	GND			
P14	14	Not connected				
P15	14	Not connected				
R1	POWER	GND	GND			
R2	34	GPIO_LED2	D6			
R3	34	GPIO_LED1	D5			
R4	34	GPIO_PSW	SW1			
R5	14	Not connected				
R6	14	Not connected				
R7	14	Not connected				
R8	14	Not connected				
R9	14	FPGA_PULLUP	BANK14_VCCO			
R10	14	FPGA_PULLDOWN	GND			
R11	14	Not connected				

Pin #	BANK	Name	Connect 1	Connect 2	Connect 3	Connect 4
R12	14	Not connected				
R13	14	Not connected				
R14	14	Not connected				
R15	POWER	GND	GND			